

Fig. 1
Prior Art

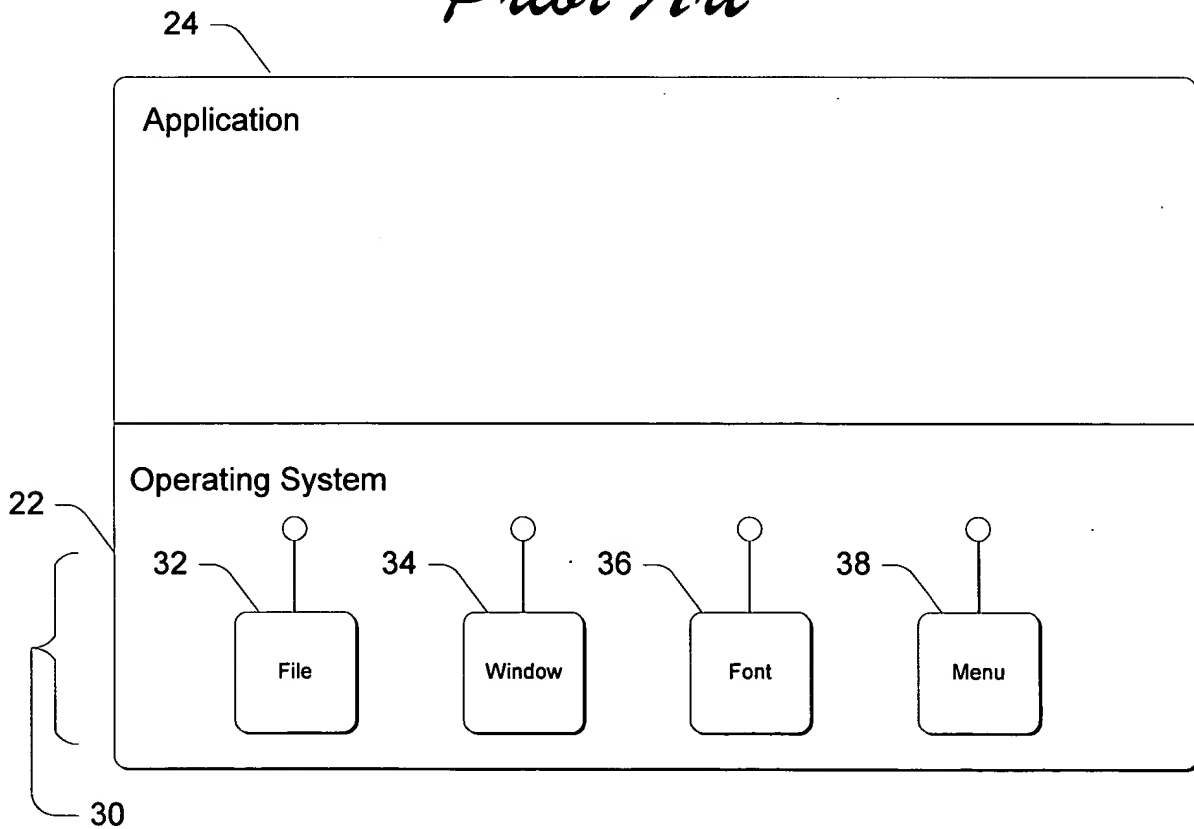
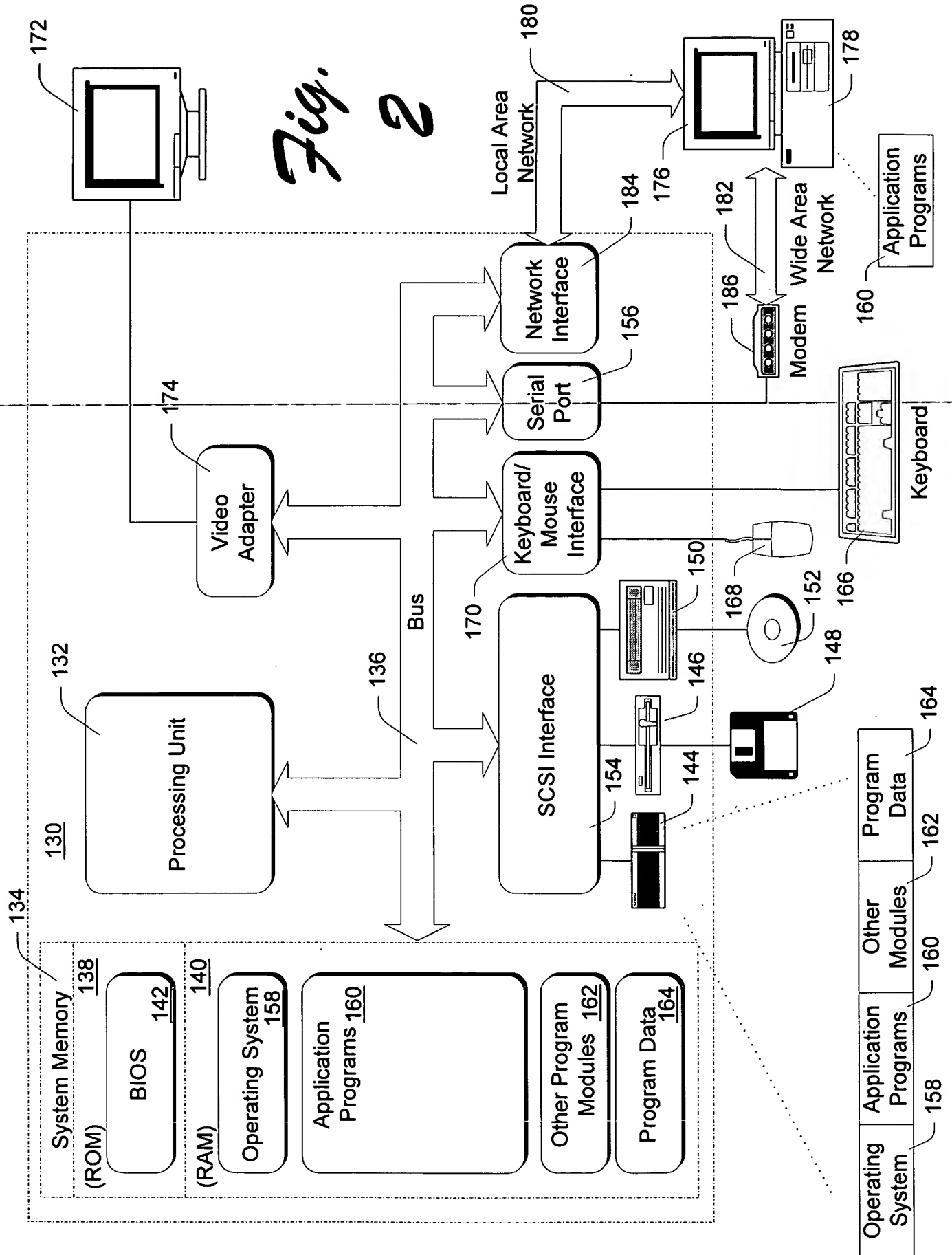
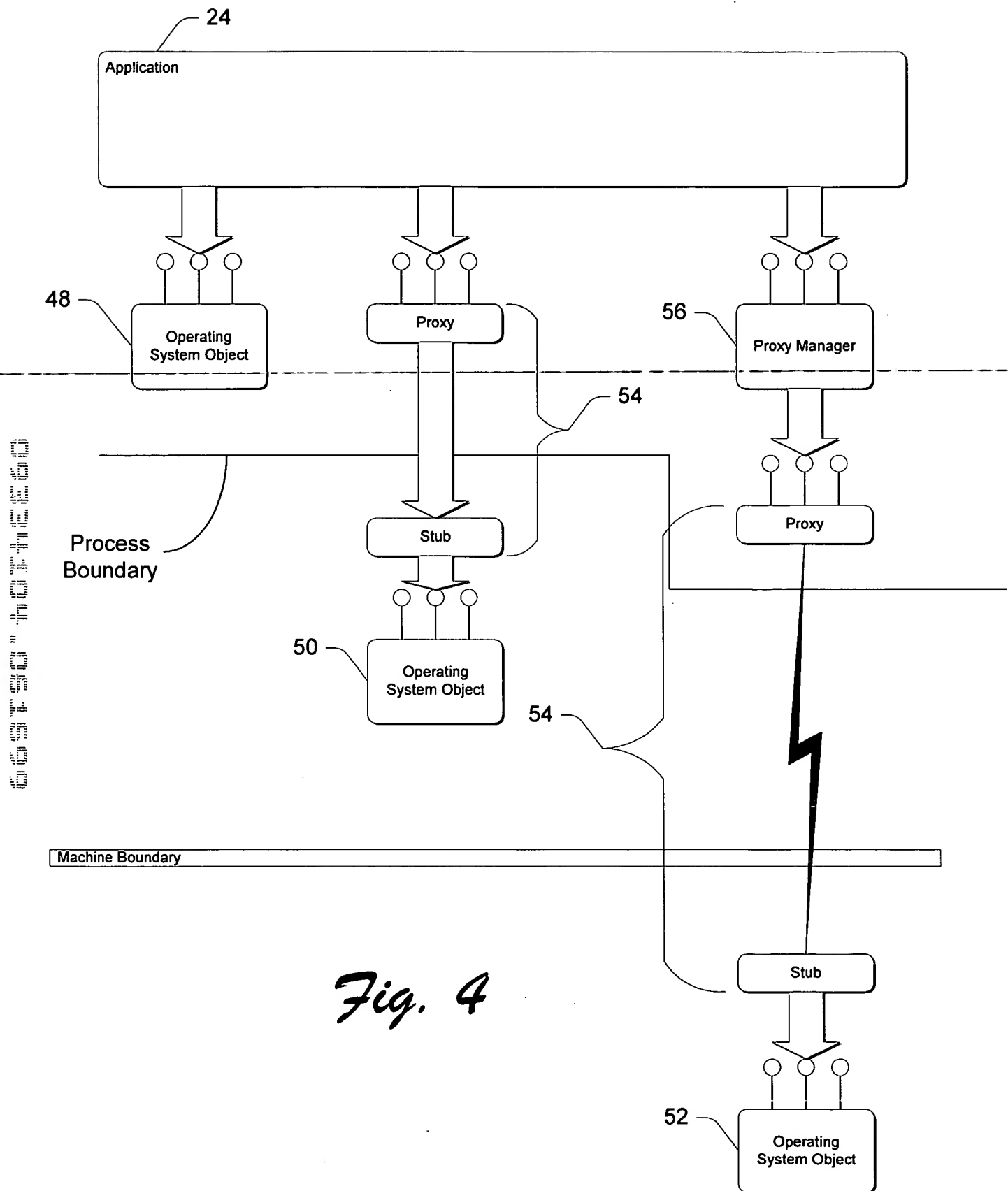


Fig. 3

Fig. 2





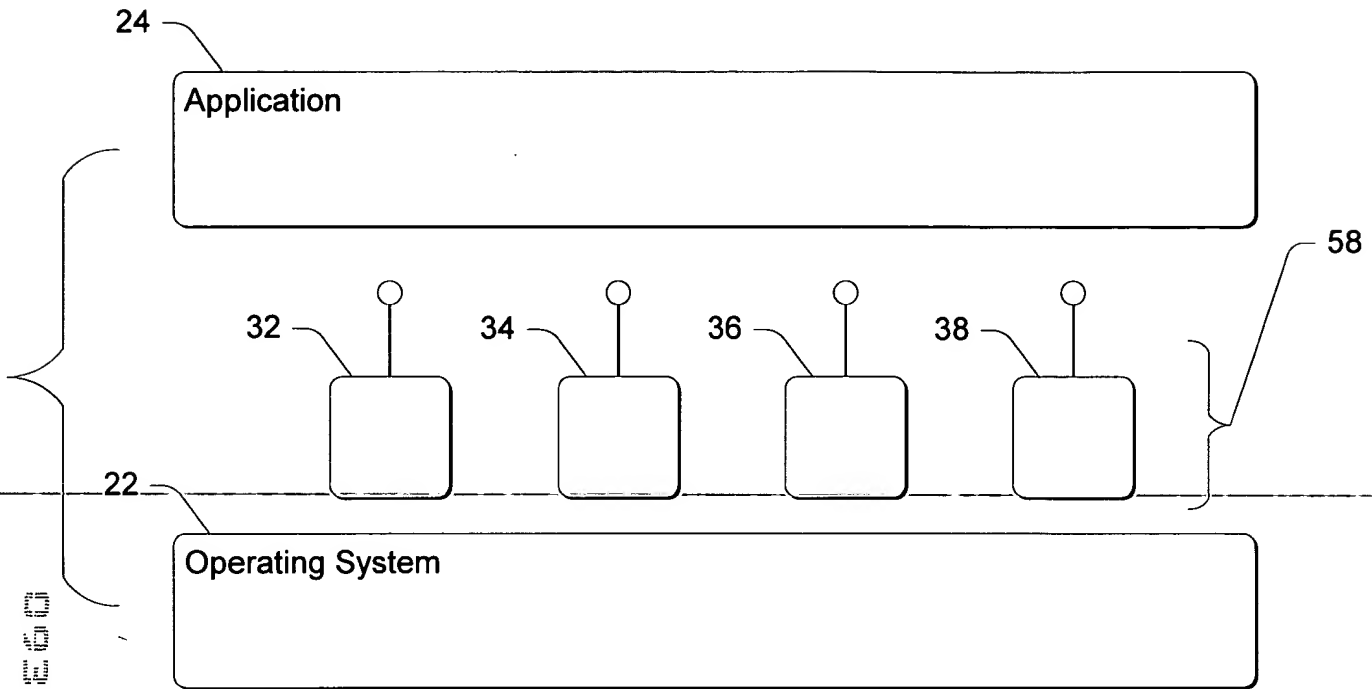


Fig. 5

FIG. 5 is a high-level system architecture diagram.

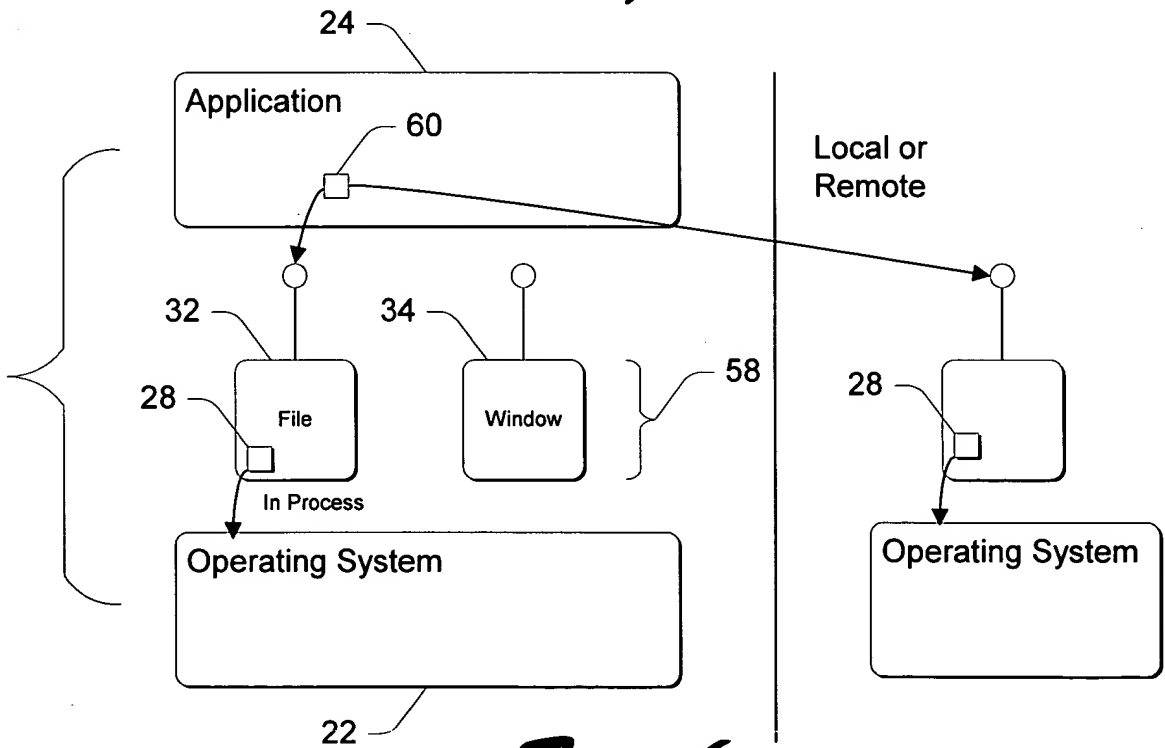


Fig. 6

MS1-354US

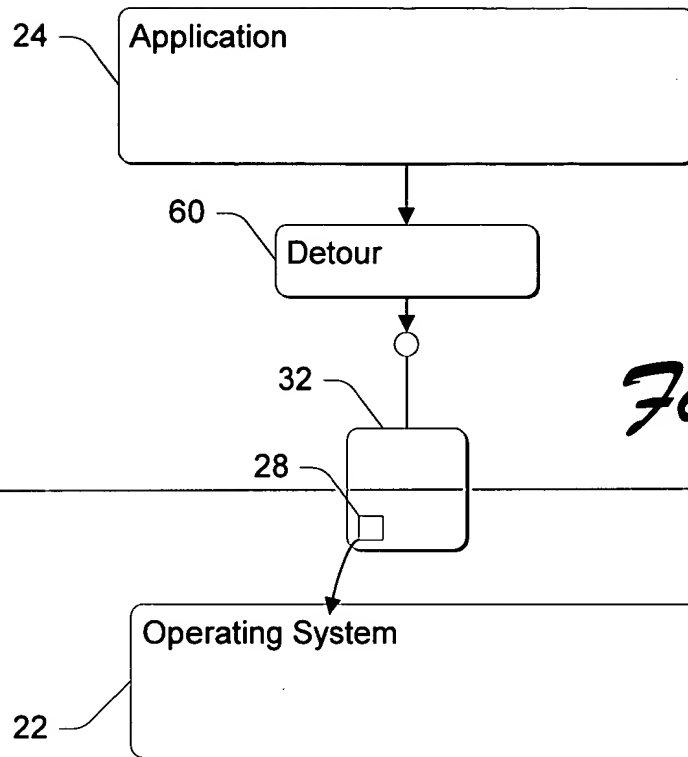


Fig. 7

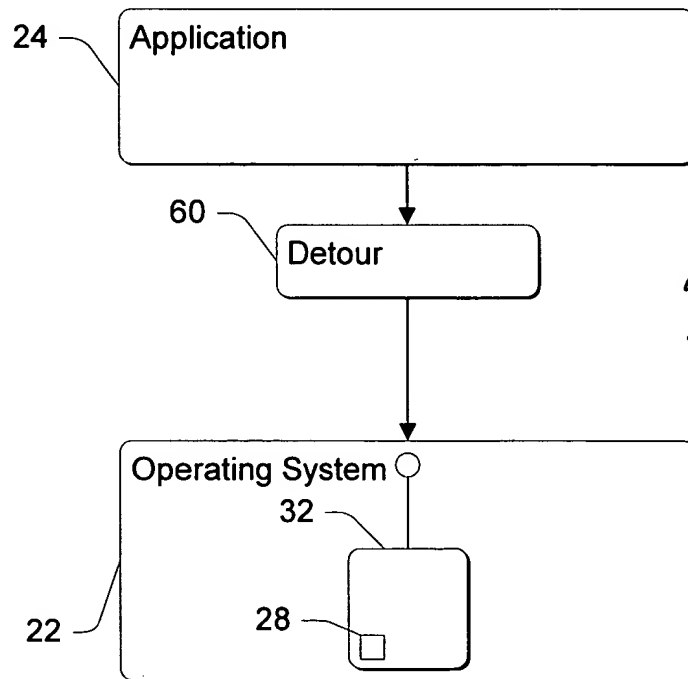


Fig. 8

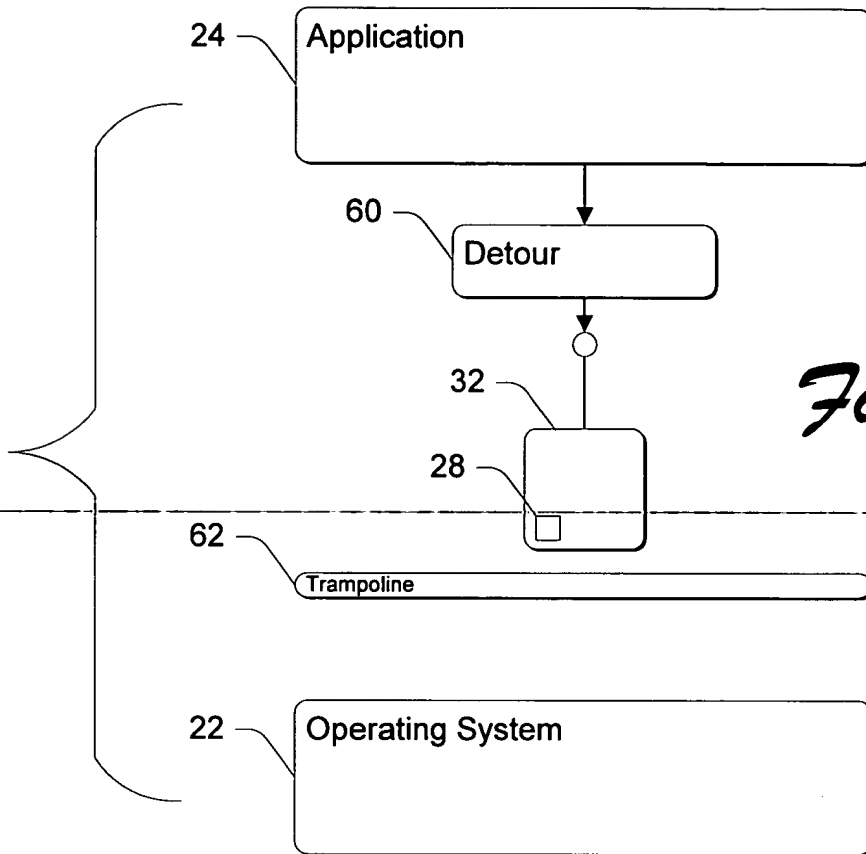


Fig. 9

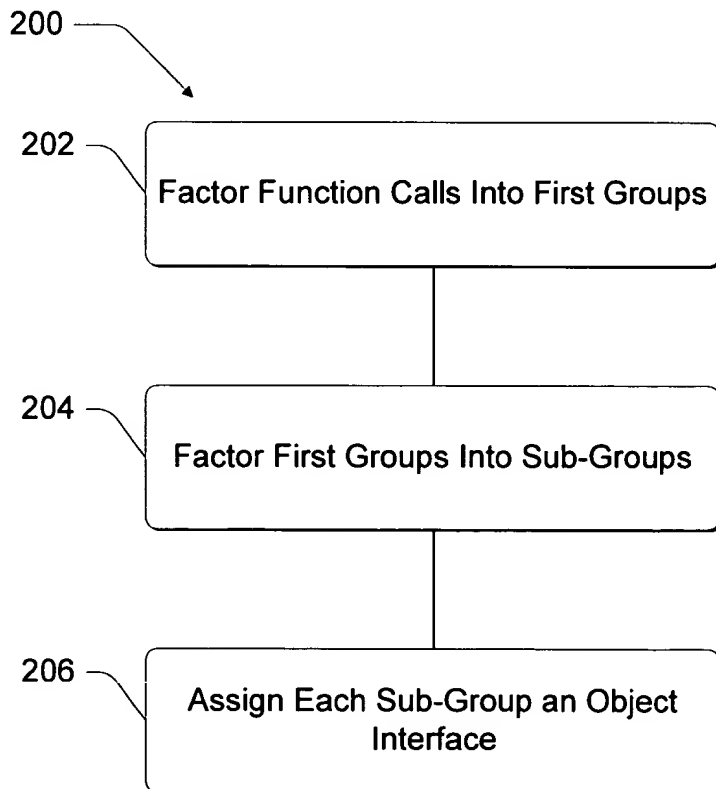


Fig. 10

MS1-354US

6333-107-0000

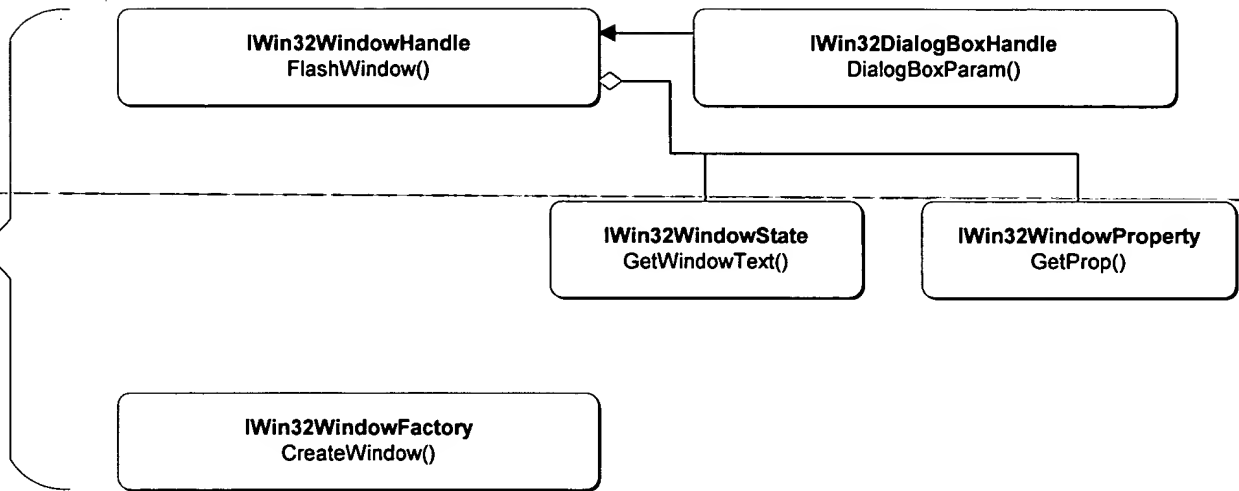


Fig. 11

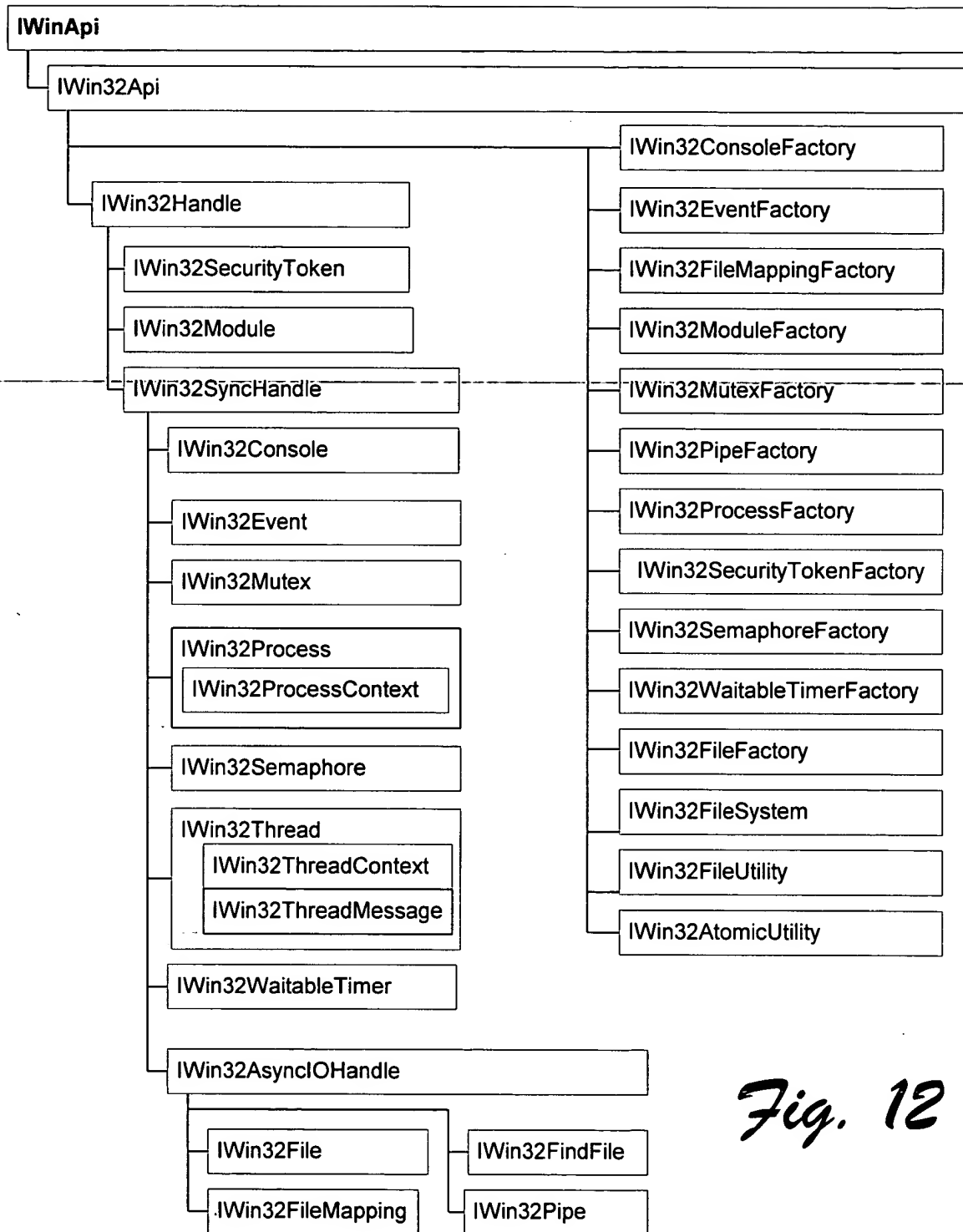


Fig. 12



Fig. 13

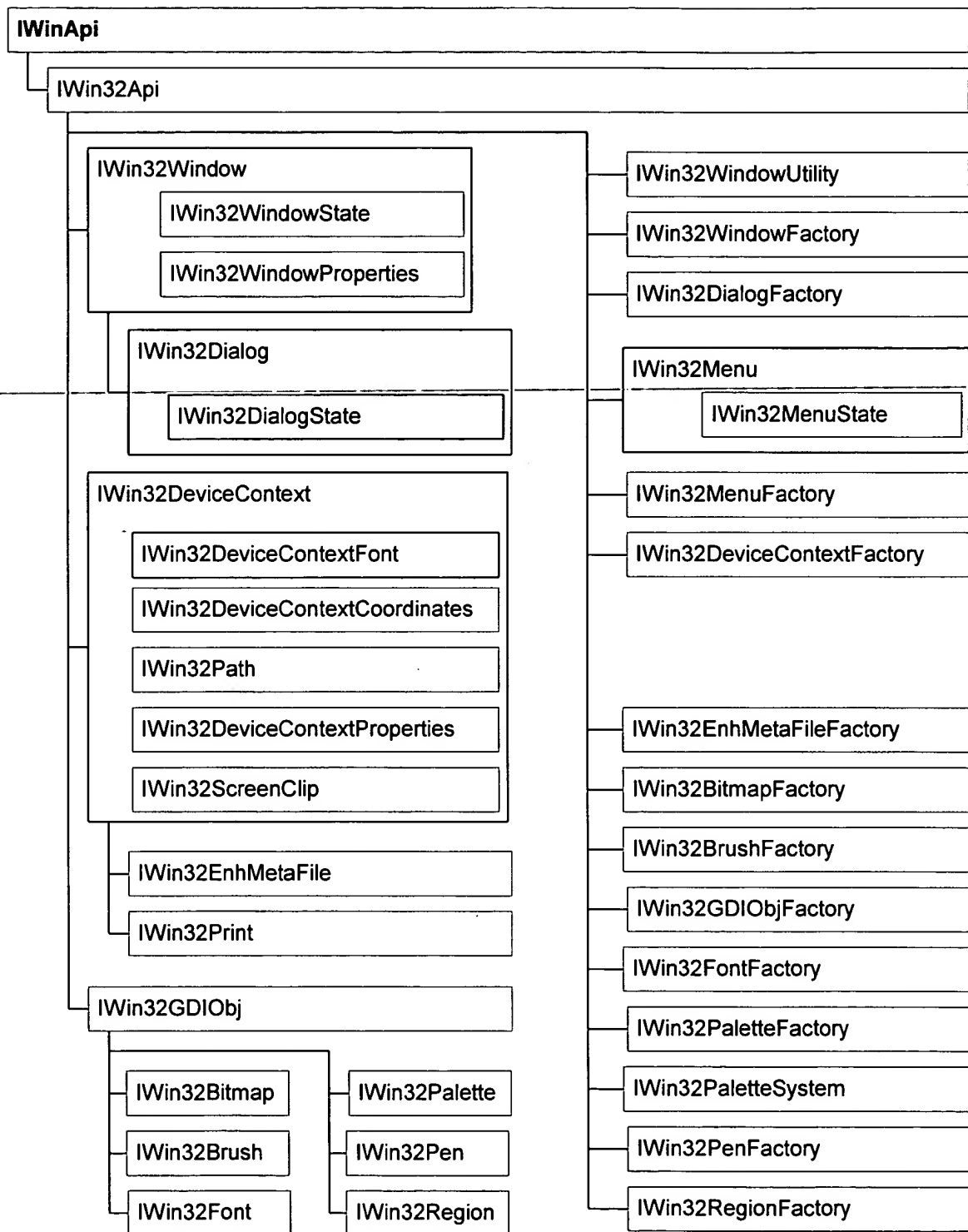


Fig. 14

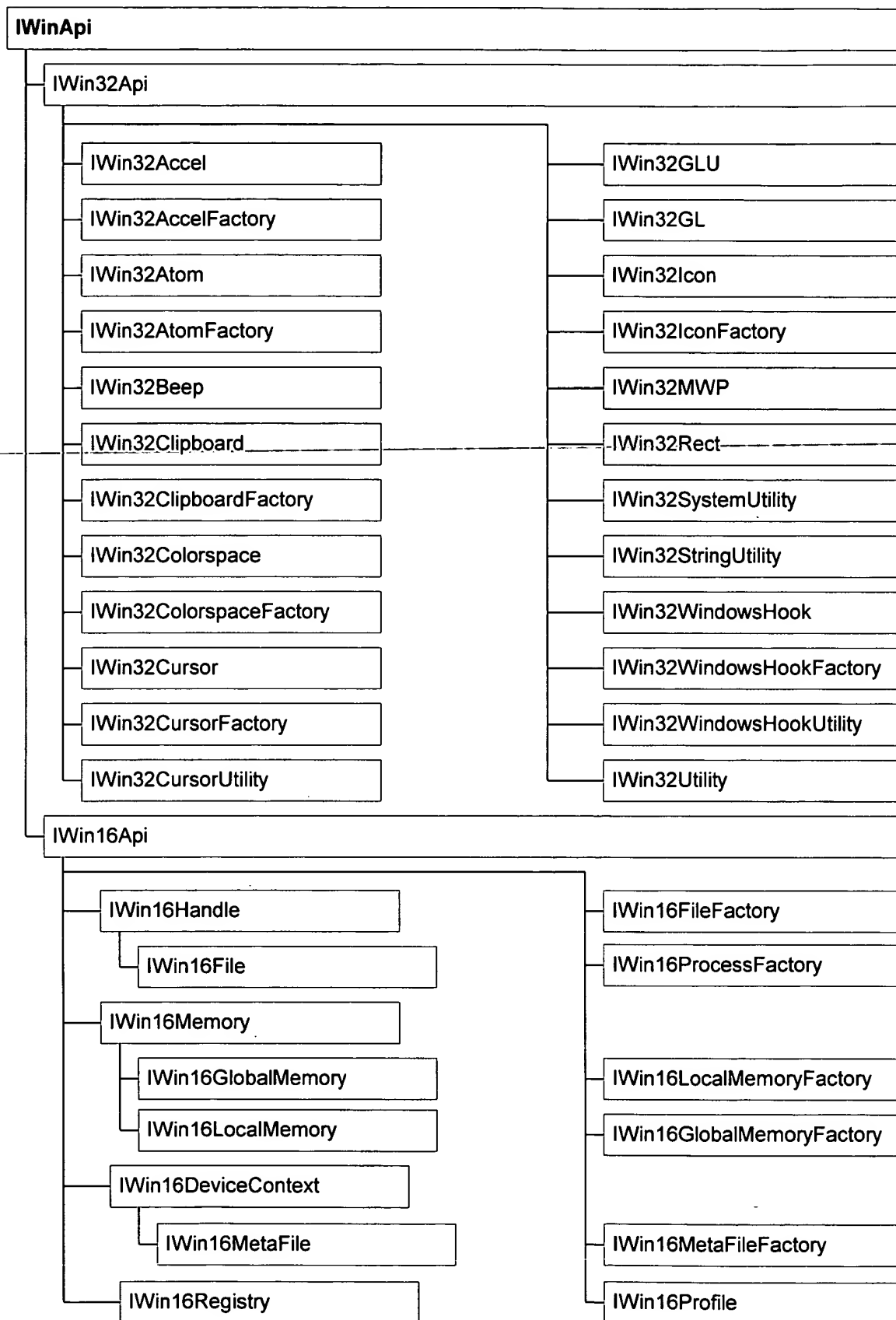


Fig. 15